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Modular data storage with Anvil

Mike Mammarella, Shant Hovsepian, Eddie Kohler

October SOSP '09: Proceedings of the ACM SIGOPS 22nd symposium on Operating systems principles 2009

Publisher: ACM & Request Permissions

Full text available: Pdf (282.26 Additional Information: full citation, abstract, references, index terms KB)

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Databases have achieved orders-of-magnitude performance improvements by changing the layout of stored data -- for instance, by arranging data in columns or compressing it before storage. These improvements have been implemented in monolithic new engines, ...

Keywords: databases, modular design, software architecture

Scalable security for large, high performance storage systems

Andrew W. Leung, Ethan L. Miller

October StorageSS '06: Proceedings of the second ACM workshop on Storage security and survivability 2006

Publisher: ACM Name Request Permissions

Full text available: Pdf (449.27 Additional Information: full citation, abstract, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 13, Downloads (12 Months): 84, Downloads (Overall): 271, Citation Count: 4

New designs for petabyte-scale storage systems are now capable of transferring hundreds of gigabytes of data per second, but lack strong security. We propose a scalable and efficient protocol for security in high performance, object-based storage systems ...

Keywords: capabilities, object-based storage, scalability

Stasis: flexible transactional storage

Russell Sears, Eric Brewer

November OSDI '06: Proceedings of the 7th symposium on Operating systems design and

2006 implementation Publisher: USENIX Association Full text available: Pdf (492.74

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 63, Downloads (Overall): 143, Citation Count: 1

An increasing range of applications requires robust support for atomic, durable and concurrent transactions. Databases provide the default solution, but force applications to interact via SQL and to forfeit control over data layout and access mechanisms. ...

Router plugins: a software architecture for next-generation routers

<u>Dan Decasper, Zubin Dittia, Guru Parulkar, Bernhard Platiner</u>

February IEEE/ ACM Transactions on Networking (TON), Volume 8 Issue 1

2000

Publisher: IEEE Press

Full text available: Pdf (530.34 Additional Information: full citation, references, cited by, index terms

Bibliometrics: Downloads (6 Weeks): 9, Downloads (12 Months): 57, Downloads (Overall): 764, Citation Count: 8

Keywords: communication system routing, communication system security, internet, modular computer systems

Hiding I/O latency with pre-execution prefetching for parallel applications

<u>Yong Chen, Surendra Byna, Xian-He Sun, Rajeev Thakur, William Groop</u>

SC '08: Proceedings of the 2008 ACM/IEEE conference on Supercomputing

2008

Publisher: IEEE Press

Full text available: Pdt (1.12 Additional Information: full citation, abstract, references, index terms MB)

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 219, Downloads (Overall): 278, Citation Count: 0

Parallel applications are usually able to achieve high computational performance but suffer from large latency in I/O accesses. I/O prefetching is an effective solution for masking the latency. Most of existing I/ O prefetching techniques, however, are ...

Column-stores vs. row-stores: how different are they really?

<u>Daniel J. Abadi, Samuel R. Madden, Nabil Hachem</u>

June SIGMOD '08: Proceedings of the 2008 ACM SIGMOD international conference on Management of

2008 data

Publisher: ACM & Request Permissions

Full text available: [10] (808.86 KB)

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 76, Downloads (12 Months): 460, Downloads (Overall): 777, Citation Count: 6

There has been a significant amount of excitement and recent work on column-oriented database systems ("column-stores"). These database systems have been shown to perform more than an order of magnitude better than traditional row-oriented database systems ...

Keywords: c-store, column-oriented dbms, column-store, compression, invisible join, tuple materialization, tuple reconstruction

Reconciling performance and programmability in networking systems

Jayaram Mudigonda, Harrick M. Vin, Stephen W. Keckler

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2007 and protocols for computer communications

Publisher: ACM Request Permissions

Full text available: [7] (380.93 κ̈́B)

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 87, Downloads (Overall): 291, Citation Count: 2

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Keywords: data cache, memoty bottleneck, multithreading, packet processing, processor architectures, reconfigurable architectures, routers

Also published in:

October 2007 SIGCOMM Computer Communication Review Volume 37 Issue 4

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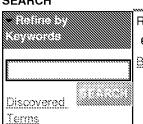
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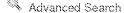
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1 An adaptable multithreaded prefetching technique for client-server object bases

<u>Niis Knafla</u> January

Cluster Computing, Volume 1 Issue 1

1998

Publisher: Kluwer Academic Publishers

Full text available: publisher

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

Given the existence of powerful multiprocessor client workstations in many client-server object database applications, the performance bottleneck is the delay in transferring pages from the server to the client. We present a prefetching ...

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Keywords: databases, modular design, software architecture

3 Toward the parallelization of GSL

Jose Ignacio Aliaga, Francisco Almeida, Jose Manuel Badía, Sergio Barrachina, Vicente Blanco, Maria Castillo, Rafael Mayo, Enrique S. Quintana, Gregorio Quintana, Alfredo Remón, Casiano Rodríguez, Francisco Sande, Adrian Santos

April The Journal of Supercomputing , Volume 48 Issue 1 2009

Publisher: Kluwer Academic Publishers

Additional Information: full citation, abstract, references

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

In this paper, we present our joint efforts to design and develop parallel implementations of the GNU Scientific Library for a wide variety of parallel platforms. The multilevel software architecture proposed provides several interfaces: a sequential ...

Keywords: GNU Scientific Library, Numerical scientific computing, Parallel algorithms and architectures, Web services

4 Scalable security for large, high performance storage systems.

Andrew W. Leung, Ethan L. Miller

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5 BORG: block-reORGanization for self-optimizing storage systems

Medha Bhadkamker, Jorge Guerra, Luis Useche, Sem Burnett, Jason Liptak, Baiu Bangaswami, Vagelis Hristidis

February FAST '09: Proccedings of the 7th conference on File and storage technologies

2009

Publisher: USENIX Association

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 1

This paper presents the design, implementation, and evaluation of BORG, a self-optimizing storage system that performs automatic block reorganization based on the observed I/O workload. BORG is motivated by three characteristics of I/O workloads: non-uniform ...

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Russell Sears, Eric Brewer

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2006 implementation **Publisher:** USENIX Association

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7 Hyperion: high volume stream archival for retrospective guerying

Peter J. Desnoyers, Prashant Shenoy

June ATC'07: 2007 USENIX Annual Technical Conference on Proceedings of the USENIX Annual

2007 Technical Conference **Publisher:** USENIX Association

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 1

Network monitoring systems that support data archiving and after-the-fact (retrospective) queries are useful for a multitude of purposes, such as anomaly detection and network and security forensics. Data archiving for such systems, however, is complicated ...

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Keywords: c-store, column-oriented dbms, column-store, compression, invisible join, tuple materialization, tuple reconstruction

11 Cumulvs: Interacting with High-Performance Scientific Simulations, for Visualization, Steering and

Fault Tolerance

James A. Kohl, Torsten Wilde, David E. Bernholdt

May International Journal of High Performance Computing Applications, Volume 20 Issue 2

2006

Publisher: Sage Publications, Inc.

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

High-performance computer simulations are an increasingly popular alternative or complement to physical experiments or prototypes. However, as these simulations grow more massive and complex, it becomes challenging to monitor and control their execution. ...

Keywords: CCA, CUMULVS, ECho, Global Arrays, MPI, MxN, PVM, computational steering, fault tolerance, model coupling, visualization

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Jayaram Mudigonda, Harrick M. Vin, Stephen W. Keckler

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Also published in:

October 2007 SIGCOMM Computer Communication Review Volume 37 Issue 4

13 Clotho: decoupling memory page layout from storage organization

<u> Minglong Shao, Jirí Schindler, Steven W. Schlosser, Anastassía Allamakí, Gregory R. Ganger</u>

August VLDB '04: Proceedings of the Thirtieth international conference on Very large data

2004 bases - Volume 30, Volume 30

Publisher: VLDB Endowment Full text available: (212.45

KB)

12.45 Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>index terms</u>

Bibliometrics: Downloads (6 Weeks): 4, Downloads (12 Months): 30, Downloads (Overall): 42, Citation Count: 1

As database application performance depends on the utilization of the memory hierarchy, smart data placement plays a central role in increasing locality and in improving memory utilization. Existing techniques, however, do not optimize accesses to all ...

14 Global memory management for a multi computer system

<u>Dejan Milojicic, Steve Hoyle, Alan Messer, Albert Munoz, Lance Russell, Tom Wylegala, Vivekanand Vellanki, Stephen Childs</u>

August WSS'00: Proceedings of the 4th conference on USENIX Windows Systems Symposium -

2000 **Volume 4**, Volume 4 **Publisher:** USENIX Association

Additional Information: full citation, abstract, references, index terms

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Downloads (Overall): n/a, Citation Count: 0

In this paper, we discuss the design and implementation of fault-aware Global Memory Management (GMM) for a multikernel architecture. Scalability of today's systems is limited by SMP hardware, as well as by the underlying commodity operating systems ...

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